

NPN SILICON RF POWER TRANSISTOR

DESCRIPTION:

The **ASI ALR030** is Designed for

FEATURES:

- Input Matching Network
- **Omnigold™** Metalization System

MAXIMUM RATINGS

I_C	2.75 A
V_{CC}	32 V
P_{DISS}	63 W @ T _C = 25 °C
T_J	-65 °C to +250 °C
T_{STG}	-65 °C to +200 °C
θ_{JC}	2.4 °C/W

PACKAGE STYLE .310 2L FLG

DIM	MINIMUM inches / mm	MAXIMUM inches / mm
A	.095 / 2.41	.105 / 2.67
B	.100 / 2.54	.120 / 3.05
C	.050 / 1.27	
D	.286 / 7.26	.306 / 7.77
E	.110 / 2.79	.130 / 3.30
F	.306 / 7.77	.318 / 8.08
G	.148 / 3.76	
H	.400 / 10.16	
I	.119 / 3.02	
J	.552 / 14.02	.572 / 14.53
K	.790 / 20.07	.810 / 20.57
L	.300 / 7.62	.320 / 8.13
M	.003 / 0.08	.006 / 0.15
N	.052 / 1.32	.072 / 1.83
P	.118 / 3.00	.131 / 3.33
R		.230 / 5.84

ORDER CODE: ASI10512

CHARACTERISTICS T_C = 25 °C

SYMBOL	TEST CONDITIONS	MINIMUM	TYPICAL	MAXIMUM	UNITS
BV_{CBO}	I _C = 10 mA	55			V
BV_{CER}	I _C = 20 mA R _{BE} = 10 Ω	55			V
BV_{EBO}	I _E = 1 mA	3.5			V
I_{CES}	V _E = 28 V			5	mA
h_{FE}	V _{CE} = 5.0 V I _C = 1.0 A	15		150	---
P_G	V _{CC} = 28 V P _{OUT} = 30 W f = 1.2 to 1.4 GHz	7.0			dB
η_C		45			%